EAST Search History

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S28	2	"5634058".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/18 21:26
S29	2	"20040194059"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 12:29
S30	2	"20020166081"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 12:36
S31	7	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)) same path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:08
S32	2	"20050022155"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:10
S34	19	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:11
S35	8	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB	OR	ON	2009/04/20 13:11

S36	-	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm. and (717/123 717713/ 717/143 717/144717/155 717/156)	US-PGPUB	OR	ON	2009/04/20 13:13
S 37	1	(pars\$4 analyz44) with (description) same ((Indl) (vhdl) (hardware adj description adj language) (verilog)). clanuad (717/123 717/147/17/155 717/146)	US-PGPUB	OR	ON	2009/04/20 13:15
S42	4	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) and (717/123 717/13/ 717/143 717/144717/155 717/156)	US-PGPUB	OR	ON	2009/04/20 13:16
S44	11	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path and (717/123 717/13/ 717/1443 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB	OR OR	ON	2009/04/20 13:18
S45		(net\$list) same ((hdl) (vhdl) (hardware adj description adj larguage) (verilog)) same path.clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18)	US-PGPUB	OR	ON	2009/04/20 13:19

S46	4	(net\$ist) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path.clm. and (717/123 717/13/ 717/1443 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:19
S47	13	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4.clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:29
S49	11	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and path).dm. and (717/123 717/13/ 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; US-PAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20
S50	2	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language) and path), clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PCPUB; US-PAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:31

S51	, , , , , , , , , , , , , , , , , , ,	(((hd) (vhdl) ((hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtll) and path).clm. and (717/123 717/13/ 717/144717/155 717/145 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:31
S53	22	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path) and (717/123 717/13/ 717/144717/155 717/145 717/137 716/3 716/5 716/16	US-PGPUB; US-PAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:32
S54	4	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path and net \$list) and (717/123 717/137 717/143 717/156 717/156 717/157 716/13 716/18 716/16 716/16	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:33

S 55	14	((((hd)) (vhd)) (hardware adj description adj language) (verilogi)) same (register near1 transfer near1 transfer near1 language (rII)) same (simulat\$4 test\$4) same path and net \$ist) and (717101 717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; US-PGT; FPFS; EPO; JPO; DET-WENT; IBM_TDB	OR	ON	2009/04/20 13:33
S56		(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat84 test84) same (design adj file path) and net\$iist) and (717/101 717/123 717/13/ 717/143 717/143/ 717/155 717/156 717/137 716/3 716/16	US-PGPUB; USPAT; FPRS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:34
S57	14	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$list) and (717/101 717/123 717/137 717/143 717/144717/155 717/156 716/16	US-PGPUB; USPAT; FPFS; EPC; JPC; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:34

S58	14	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$list) and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18)	US-PGPUB; USPAT; FPFS; EPO; JPO; DETWIDNT; IBM_TDB	OR	ON	2009/04/20 13:43
S59	91	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rII)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$list (file adj path)) and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPFS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:46
S60		(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 alnguage (rtl)) same (simulat\$4 test\$4) same (lo near2 design) and net\$iist (file adj path) and (rapid \$make)) and (717/101 717/123 717/138 717/144717/155 717/156 717/157 71/16/3 716/5 716/16 716/18)	US-PGPUB; USPAT; FPRS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/20 13:51

S61	-	((((nd)) (vhdl) ((hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rll)) same (simulat\$4 test\$4) same (1c near2 design) and net\$list (file adj path) and (rapid \$make)) and (7r7/101 717/123 717/138 717/143 717/143 716/15 717/156 717/137 716/3 716/5 716/16	US-PGPUB	OR	ON	2009/04/20 13:51
S63	»	(((hdl) (vhdl) (hardware adj description adj larguage) (verilog) asci) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (to near2 design) and net\$list (file adj path)).clm. and (717/101 717/123 717/138 717/143 717/1447/17/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPFS; EPC; JPC; DERWENT; IBM_TDB	OR OR	ON	2009/04/20 13:56
S64	33	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and (717/123 717/13/717/143 717/145 717/156 717/157 717/167 716/16 716/18)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:36

S65	51	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16	US-PCPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 14:36
S67	114	path same ((hdl) (vhdl) (hardware adj description adj alanguage) (verilog) rtl) same compil\$\foats4 and index and (717/123 717/14/717/155 717/163 717/143 717/147/155 717/163 716/16 716/16	US-PGPUB; USPAT; FPPS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 14:37
S69	40	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compilis4 and (integrated adj circuit) and (717/123 717/13/717/143 717/156 717/156 717/163 716/16 716/18)	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20 14:38
S70	. 6	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 same (integrated adj circuit) and (717/123 717/13/ 717/143 717/156 717/156 717/163 716/16 716/16)	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFMENT; IBM_TDB	OR	ON	2009/04/20 14:44

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